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Application ID:

10684713

Title of Invention:

Vertical Replacement-Gate (VRG)

Multiple Operating Voltage

Transistor

First Named Inventor:

Paul Layman

Domestic/Foreign Application: Domestic Application

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TRANSMITTAL

Electronic Version v1.1 Stylesheet Version v1.1.0

> Title of Invention

Multiple Operating Voltage Vertical Replacement-Gate (VRG) Transistor

Application Number:

10/684713

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First Named Applicant: Paul Arthur Layman

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Attorney Docket Number: 075903-282

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Submitted by:	Elec. Sign.	Sign. Capacity
John L. DeAngelis Jr. Registered Number: 30,622	/John L. DeAngelis, Jr./	Attorney

Documents being submitted

Files

us-ids

Agere282-usidst.xml

us-ids.dtd

us-ids.xsl

Comments



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

Multiple Operating Voltage Vertical Replacement-Gate (VRG) Transistor

Application Number:

10/684713

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First Named Applicant: Attorney Docket Number: 075903-282

Paul Layman

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(4670768 or 5010386 or 5140388 or 5502009 or 5861347 or 6300199 or 6309930 or 6387758 or 4366495 or 4455565 or 4587713 or 4683643 or 4786953 or 4837606 or 5342797 or 6027975 or 6072216 or 6133099 or 6197641 or 6133164 or 5414289 or 5576238 or 5668391 or 5744846 or 6297531 or 4951102 or 5208172 or 5545586 or 5554870 or 5864158 or 6268621).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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Signature

Examiner Name	Date